Abstract:

Device and Method for Analyzing Embedded Systems

Disclosed is an analysis device for an embedded system (9) including a CPU (1), a CPU bus (2) and a memory (3). The embedded system has at least one communication module (4) for the input or output of analysis data by way of a test interface (5). The communication module permits the internal memory and the input and output access operations of the embedded system to be monitored and/or logged without using the clock cycles of the CPU (1).